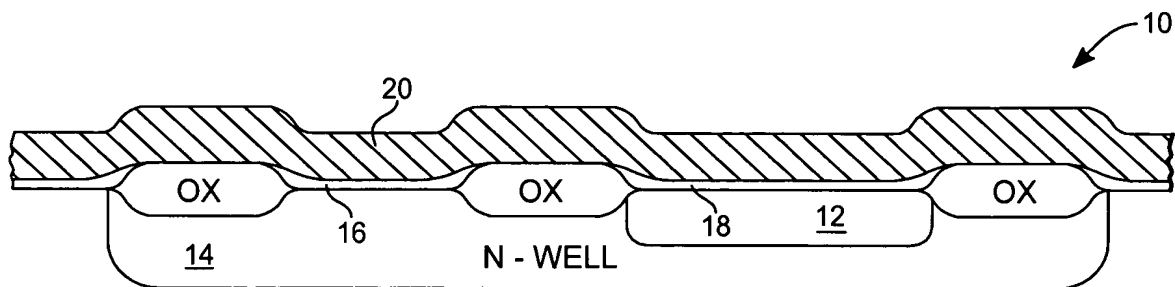
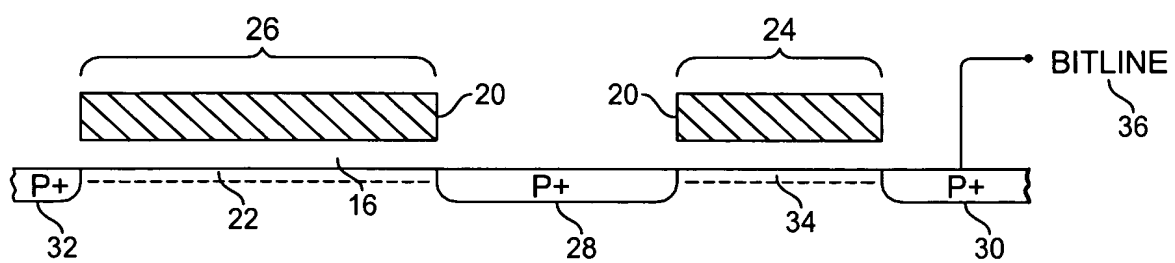


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*Fig. 1A (prior art)*



*Fig. 1B (prior art)*

+

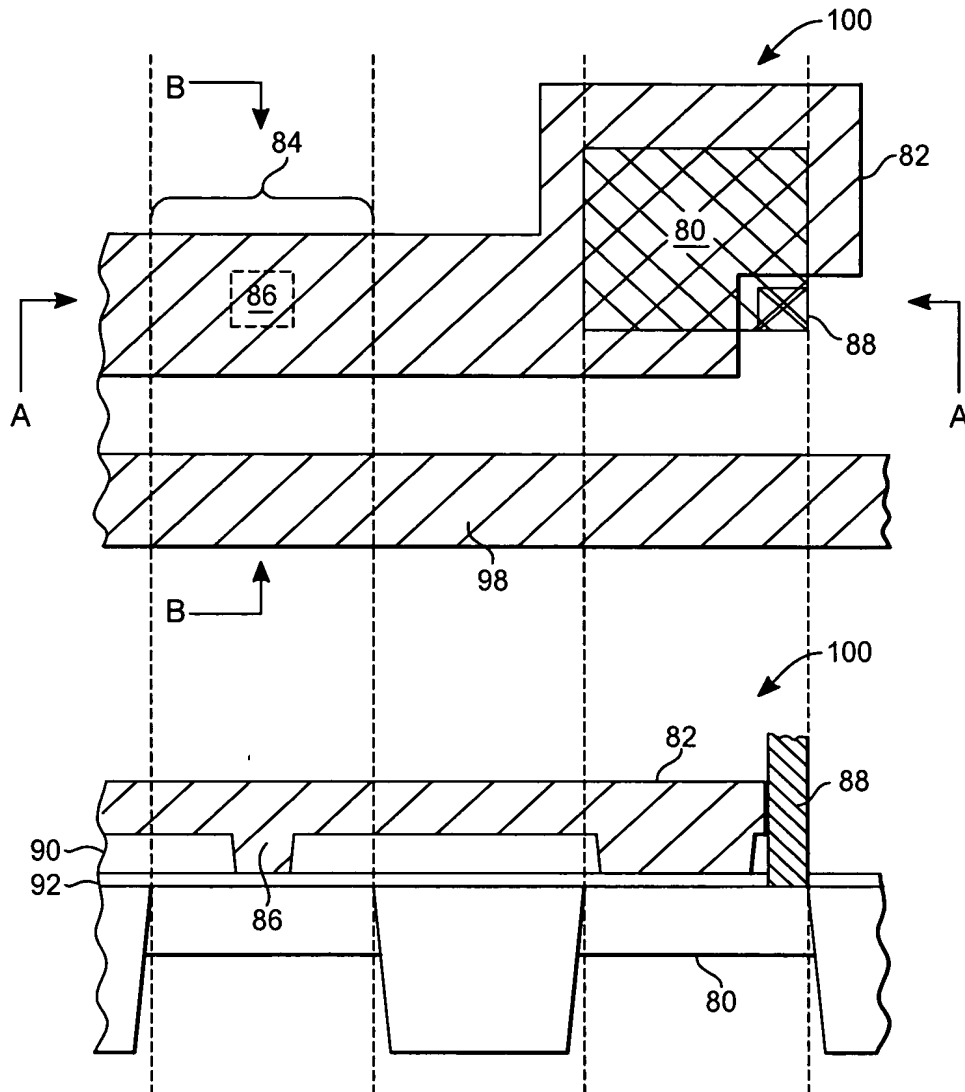
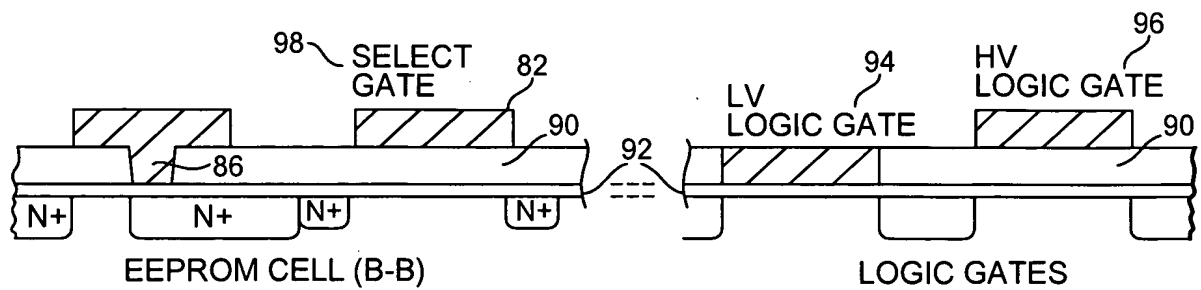


Fig. 2A

Fig. 2B

EEPROM CELL (A-A)



EEPROM CELL (B-B)

LOGIC GATES

Fig. 2C





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